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<input type="checkbox"/>	L1	(cable bundle? or wiring bundle?) and layout	51

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Search Results - Record(s) 1 through 20 of 20 returned.

☐ 1. Document ID: US 20020191552 A1

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L2: Entry 1 of 20

File: PGPB

Dec 19, 2002

PGPUB-DOCUMENT-NUMBER: 20020191552

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020191552 A1

TITLE: Interference cancellation equipment

PUBLICATION-DATE: December 19, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Watkinson, Peter Geoffrey	Cambridge		GB	

US-CL-CURRENT: 370/286; 370/317

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw D
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☐ 2. Document ID: US 20020010735 A1

L2: Entry 2 of 20

File: PGPB

Jan 24, 2002

PGPUB-DOCUMENT-NUMBER: 20020010735

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020010735 A1

TITLE: Multicast transmissions in a multistage interconnect network

PUBLICATION-DATE: January 24, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
McMillen, Robert J.	Encinitas	CA	US	
Watson, M. Cameron	Los Angeles	CA	US	
Chura, David J.	Redondo Beach	CA	US	

US-CL-CURRENT: 709/201; 370/254

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw D
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☐ 3. Document ID: US 6567963 B1

L2: Entry 3 of 20

File: USPT

May 20, 2003

US-PAT-NO: 6567963

DOCUMENT-IDENTIFIER: US 6567963 B1

TITLE: Wafer scale integration and remoted subsystems using opto-electronic transceivers

DATE-ISSUED: May 20, 2003

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Trezza; John A.	Nashua	NH		

US-CL-CURRENT: 716/8; 716/10, 716/9

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 4. Document ID: US 6491258 B1

L2: Entry 4 of 20

File: USPT

Dec 10, 2002

US-PAT-NO: 6491258

DOCUMENT-IDENTIFIER: US 6491258 B1

TITLE: Space elevator

DATE-ISSUED: December 10, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Boyd; Robert Raymond	Arlington	VA		
Thomas; Dimitri David	Mountain View	CA		

US-CL-CURRENT: 244/158R, 244/161

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 5. Document ID: US 6407933 B1

L2: Entry 5 of 20

File: USPT

Jun 18, 2002

US-PAT-NO: 6407933

DOCUMENT-IDENTIFIER: US 6407933 B1

TITLE: Cable management system for use with rack mounted devices

DATE-ISSUED: June 18, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
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Bologna; David F.	Spring	TX
Salinas; Everett R.	Pasadena	TX
Kuo; Sung-Hsia Jim	Houston	TX
McAfee; David A.	Spring	TX

US-CL-CURRENT: 361/826; 174/168, 248/68.1, 248/74.2, 361/797, 439/572

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 6. Document ID: US 6354868 B1

L2: Entry 6 of 20

File: USPT

Mar 12, 2002

US-PAT-NO: 6354868

DOCUMENT-IDENTIFIER: US 6354868 B1

TITLE: Vehicle power distribution box

DATE-ISSUED: March 12, 2002

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Korczynski; Jacek	Niles	IL		
Spencer; Ronald Lee	Glendale	AZ		

US-CL-CURRENT: 439/516; 361/826, 439/82

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 7. Document ID: US 6243361 B1

L2: Entry 7 of 20

File: USPT

Jun 5, 2001

US-PAT-NO: 6243361

DOCUMENT-IDENTIFIER: US 6243361 B1

**\*\* See image for Certificate of Correction \*\***

TITLE: Multistage interconnect network uses a master processor to perform dynamic configuration for all switch nodes based on a predetermined topology

DATE-ISSUED: June 5, 2001

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
McMillen; Robert J.	Encinitas	CA		
Watson; M. Cameron	Los Angeles	CA		
Chura; David J.	Redondo Beach	CA		

US-CL-CURRENT: 370/254; 370/255

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 8. Document ID: US 5907544 A

L2: Entry 8 of 20

File: USPT

May 25, 1999

US-PAT-NO: 5907544

DOCUMENT-IDENTIFIER: US 5907544 A

TITLE: Hub controller architecture and function for a multiple access-point wireless communication network

DATE-ISSUED: May 25, 1999

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rypinski; Chandos A.	Mill Valley	CA	94941	

US-CL-CURRENT: 370/337; 370/342, 370/344, 370/347, 455/517

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawings
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☐ 9. Document ID: US 5872904 A

L2: Entry 9 of 20

File: USPT

Feb 16, 1999

US-PAT-NO: 5872904

DOCUMENT-IDENTIFIER: US 5872904 A

TITLE: Computer system using a master processor to automatically reconfigure faulty switch node that is detected and reported by diagnostic processor without causing communications interruption

DATE-ISSUED: February 16, 1999

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
McMillen; Robert J.	Encinitas	CA		
Watson; M. Cameron	Los Angeles	CA		
Chura; David J.	Redondo Beach	CA		

US-CL-CURRENT: 714/4; 370/217, 709/239, 714/3, 714/31

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawings
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☐ 10. Document ID: US 5836281 A

L2: Entry 10 of 20

File: USPT

Nov 17, 1998

US-PAT-NO: 5836281

DOCUMENT-IDENTIFIER: US 5836281 A

TITLE: Apparatus for optimizing the interconnection paths of elongate conduits

DATE-ISSUED: November 17, 1998

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Godeau; Denis	Joudry			FR
Garcia; Anthony	Villemandeur			FR

US-CL-CURRENT: 123/195A; 123/41.54, 123/41.55

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
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☐ 11. Document ID: US 5817980 A

L2: Entry 11 of 20

File: USPT

Oct 6, 1998

US-PAT-NO: 5817980

DOCUMENT-IDENTIFIER: US 5817980 A

TITLE: Connector element for variably laying and protecting electrical cables

DATE-ISSUED: October 6, 1998

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kirma; Safa	Wedel/Holstein			DE

US-CL-CURRENT: 174/86; 174/68.3, 174/89, 285/243, 285/249

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
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☐ 12. Document ID: US 5815089 A

L2: Entry 12 of 20

File: USPT

Sep 29, 1998

US-PAT-NO: 5815089

DOCUMENT-IDENTIFIER: US 5815089 A

TITLE: Device for transmitting signals from position detector and method of such signal transmission

DATE-ISSUED: September 29, 1998

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Katagiri; Takashi	Nagano			JP
Momose; Tetsuo	Nagano			JP

US-CL-CURRENT: 340/870.03; 324/207.11, 324/207.2, 324/207.22, 324/207.23,  
340/870.02, 340/870.31, 340/870.35, 340/870.43

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw D
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☐ 13. Document ID: US 5755189 A

L2: Entry 13 of 20

File: USPT

May 26, 1998

US-PAT-NO: 5755189

DOCUMENT-IDENTIFIER: US 5755189 A

TITLE: Interconnection frame for an engine having rigid elongate conduits

DATE-ISSUED: May 26, 1998

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Godeau; Denis	Vieilles Maisons/Joudry			FR
Garcia; Anthony	Villemandeur			FR

US-CL-CURRENT: 123/41.01; 123/41.54

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw. De
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☐ 14. Document ID: US 5625353 A

L2: Entry 14 of 20

File: USPT

Apr 29, 1997

US-PAT-NO: 5625353

DOCUMENT-IDENTIFIER: US 5625353 A

TITLE: Device for transmitting signals from position detector and method of such signal transmission

DATE-ISSUED: April 29, 1997

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Katagiri; Takashi	Nagano			JP
Momose; Tetsuo	Nagano			JP

US-CL-CURRENT: 340/870.03; 324/207.11, 324/207.2, 324/207.22, 324/207.23,  
340/870.02, 340/870.31, 340/870.35, 340/870.43

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw. De
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☐ 15. Document ID: US 5522046 A

L2: Entry 15 of 20

File: USPT

May 28, 1996

US-PAT-NO: 5522046

DOCUMENT-IDENTIFIER: US 5522046 A

**\*\* See image for Certificate of Correction \*\***TITLE: Communication system uses diagnostic processors and master processor module to identify faults and generate mapping tables to reconfigure communication paths in a multistage interconnect network

DATE-ISSUED: May 28, 1996

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
McMillen; Robert J.	Encinitas	CA		
Watson; M. Cameron	Los Angeles	CA		
Chura; David J.	Redondo Beach	CA		

US-CL-CURRENT: 709/239; 370/217, 709/221, 709/224, 710/10, 714/1, 714/2, 714/25,  
714/3, 714/30, 714/31

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
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☐ 16. Document ID: US 5321813 A

L2: Entry 16 of 20

File: USPT

Jun 14, 1994

US-PAT-NO: 5321813

DOCUMENT-IDENTIFIER: US 5321813 A

TITLE: Reconfigurable, fault tolerant, multistage interconnect network and protocol

DATE-ISSUED: June 14, 1994

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
McMillen; Robert J.	Encinitas	CA		
Watson; M. Cameron	Los Angeles	CA		
Chura; David J.	Redondo Beach	CA		

US-CL-CURRENT: 714/798; 370/244, 370/388, 370/390, 370/422, 370/447

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw D
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☐ 17. Document ID: US 5303383 A

L2: Entry 17 of 20

File: USPT

Apr 12, 1994

US-PAT-NO: 5303383

DOCUMENT-IDENTIFIER: US 5303383 A

TITLE: Multiprocessor computer system

DATE-ISSUED: April 12, 1994

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Neches; Philip M.	Dayton	OH		
McMillen; Robert J.	Encinitas	CA		
Watson; M. Cameron	Los Angeles	CA		
Chura; David J.	Redondo Beach	CA		

US-CL-CURRENT: 712/43; 370/351



Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn De
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☐ 18. Document ID: US 5283107 A

L2: Entry 18 of 20

File: USPT

Feb 1, 1994

US-PAT-NO: 5283107

DOCUMENT-IDENTIFIER: US 5283107 A

TITLE: Modular multilayer interwiring structure

DATE-ISSUED: February 1, 1994

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Bayer; Thomas	Sindelfingen			DE
Greschner; Johann	Pliezhausen			DE
Hildenbrand; Willy	Magstadt			DE
Marquart; Bernd	Schonbuch			DE
Stohr; Roland R.	Nufringen			DE
Wolter; Olaf	Aidlingen			DE

US-CL-CURRENT: 428/209; 257/E23.098, 257/E23.173, 257/E23.174, 257/E25.012,  
428/210, 428/432, 428/433, 428/901

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn De
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☐ 19. Document ID: US 4765709 A

L2: Entry 19 of 20

File: USPT

Aug 23, 1988

US-PAT-NO: 4765709

DOCUMENT-IDENTIFIER: US 4765709 A

TITLE: Equipment for distributing and connecting optical fibers

DATE-ISSUED: August 23, 1988

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Suillerot; Didier	75631 Paris Cedex 13			FR
Violo; Robert	75631 Paris Cedex 13			FR

US-CL-CURRENT: 385/135; 385/59

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn De
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☐ 20. Document ID: US 4092673 A

L2: Entry 20 of 20

File: USPT

May 30, 1978

US-PAT-NO: 4092673

DOCUMENT-IDENTIFIER: US 4092673 A

TITLE: Compatible composite image process

DATE-ISSUED: May 30, 1978

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Adams; Jay W.	Burbank	CA	91504	

US-CL-CURRENT: 348/211.3; 348/584, 352/53

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Drawings
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- 1** A case study of synthesis for industrial-scale analog IP: redesign of the 77%  
 equalizer/filter frontend for an ADSL CODEC

Rodney Phelps , Michael J. Krasnicki , Rob A. Rutenbar , L. Richard Carley , James R. Hellums

**Proceedings of the 37th conference on Design automation** June 2000

A persistent criticism of analog synthesis techniques is that they cannot cope with the complexity of realistic industrial designs, especially system-level designs. We show how recent advances in simulation-based synthesis can be augmented, via appropriate macromodeling, to attack complex analog blocks. To support this claim, we resynthesize from scratch, in several different styles, a complex equalizer/filter block from the frontend of a commercial ADSL CODEC, and verify by full si ...

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**1 Crosstalk modeling for automotive harnesses**

*Smith, W.T.; Paul, C.R.; Savage, J.S.; Das, S.K.; Coopriker, A.D.; Frazier, R.K*  
Electromagnetic Compatibility, 1994. Symposium Record. Compatibility in the Loop. IEEE International Symposium on , 22-26 Aug. 1994  
Pages:447 - 452

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**1 Field demonstration of 10-Gb/s line-rate transmission on an installed transoceanic submarine lightwave cable**

*Park, Y.K.; Nguyen, T.V.; Mizuhara, O.; Chen, C.D.; Tzeng, L.D.; Yeates, P.D. Heismann, F.; Chen, Y.C.; Ehrenberg, D.G.; Feggeler, J.C.;*  
 Photonics Technology Letters, IEEE , Volume: 8 , Issue: 3 , March 1996  
 Pages:425 - 427

[\[Abstract\]](#)   [\[PDF Full-Text \(284 KB\)\]](#)   **IEEE JNL**

**2 10 Gbit/s WDM transmission measurements on an installed optical amplifier undersea cable system**

*Feggeler, J.C.; Duff, D.G.; Bergano, N.S.; Chen, C.-C.; Chen, Y.C.; Davidson, Ehrenberg, D.G.; Evangelides, S.J.; Ferguson, G.A.; Heismann, F.L.; Homsey G.M.; Kidorf, H.D.; Kissell, T.M.; Meixner, A.E.; Menges, R.; Miller, J.L., Jr.; Mizuhara, O.; Nguyen, T.V.; Nyman, B.M.; Park, Y.-K.; Patterson, W.W.; Valvo G.F.;*  
 Electronics Letters , Volume: 31 , Issue: 19 , 14 Sept. 1995  
 Pages:1676 - 1678

[\[Abstract\]](#)   [\[PDF Full-Text \(224 KB\)\]](#)   **IEEE JNL**

**3 10 Gbit/s WDM transmission measurements on an installed optical amplifier undersea cable system**

*Feggeler, J.C.; Duff, D.G.; Bergano, N.S.; Chen, C.C.; Chen, Y.C.; Davidson, Ehrenberg, D.G.; Evangelides, S.J.; Ferguson, G.A.; Heismann, F.L.; Homsey G.M.; Kidorf, H.D.; Kissell, T.M.; Meixner, A.E.; Menges, R.; Miller, J.L.; Mizuhara, O.; Nguyen, T.V.; Nyman, B.M.; Park, Y.K.; Patterson, W.W.; Valvo, G.F.;*  
 Electronics Letters , Volume: 31 , Issue: 19 , 14 September 1995  
 Pages:1676

[\[Abstract\]](#)   [\[PDF Full-Text \(224 KB\)\]](#)   **IEEE JNL**

**4 Field demonstration of 10 Gbit/s line-rate transmission on an installed**



**transoceanic submarine lightwave cable**

*Park, Y.K.; Nguyen, T.V.; Mizuhara, O.; Chen, C.D.; Tzeng, L.D.; Yeates, P.D.  
Heismann, F.; Chen, Y.C.; Ehrenberg, D.G.; Feggeler, J.C.;*

Optical Fiber Communications, 1996. OFC '96 , Feb. 25 - March 1, 1996

Pages:75 - 76

[\[Abstract\]](#)

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**1 Computer-aided design procedures for survivable fiber optic network**

*Cardwell, R.H.; Monma, C.L.; Wu, T.-H.;*

Selected Areas in Communications, IEEE Journal on , Volume: 7 , Issue: 8 , Oct 1989

Pages:1188 - 1197

[\[Abstract\]](#)   [\[PDF Full-Text \(996 KB\)\]](#)   **IEEE JNL**

**2 Crosstalk modeling for automotive harnesses**

*Smith, W.T.; Paul, C.R.; Savage, J.S.; Das, S.K.; Coopridge, A.D.; Frazier, R.K.*

Electromagnetic Compatibility, 1994. Symposium Record. Compatibility in the Loop. IEEE International Symposium on , 22-26 Aug. 1994

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**3 Feedback-driven datapath optimization in Fasolt**

*Knapp, D.W.;*

Computer-Aided Design, 1990. ICCAD-90. Digest of Technical Papers., 1990 International Conference on , 11-15 Nov. 1990

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**4 Se.C.R.E.T.S.: A stability experiment on the role of segregated copper Nb<sub>3</sub>Sn cable-in-conduit conductors**

*Bruzzzone, P.; Vecsey, G.; Shikov, A.; Pansyrny, V.; Sytnikov, V.; Oswald, B. Strasser, T.; Nijhuis, A.; Noordman, N.;*

Applied Superconductivity, IEEE Transactions on , Volume: 10 , Issue: 1 , Mar 2000

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**1 Bundled cable parameters and their impact on EMI measurement repeatability**

*Hubing, T.H.;*

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**2 Field excitation of complex multiconductor cable bundles-simulation and measurements**

*Rumold, J.; ter Haseborg, J.L.;*

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*Jung, L.; Luiken ter Haseborg, J.;*

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**7 Fault arc tests in cable bundles under microgravity conditions**

*Hanson, J.; Konig, D.; Judd, M.; Reher, H.J.;*

Dielectrics and Electrical Insulation, IEEE Transactions on [see also Electrical Insulation, IEEE Transactions on] , Volume: 7 , Issue: 6 , Dec. 2000

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**8 Analytical method of calculating the transient and steady-state temperature rises for cable-bundle in tray and ladder**

*Song-Bo Liu; Xu Han;*

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*Poudroux, C.; Rifi, M.; Demoulin, B.;*

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**11 Radiated susceptibility of wiring system aboard lightning struck aircraft. Part I: sensitivity to the p.u.l. external parameters**

*D'Amore, M.; Sarto, M.S.; Scarlatti, A.;*

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**12 Investigation of using ferrite cores to control common mode imped  
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**16 A simple high frequency, noise generated direct drive simulator**

*Lawry, D.; Prather, W.D.; Torres, R.; Tran, T.; Gutierrez, S.; Langdon, S.; Wa W.; Frazier, S.; Hoffer, G.; Kokorowski, S.; McLemore, D.P.; Peckham, V.D.;* Electromagnetic Compatibility, 1993. Symposium Record. 1993 IEEE International Symposium on , 9-13 Aug. 1993  
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**17 Ampacity of diversely loaded cables in covered and uncovered tray**

*Black, W.Z.; Harshe, B.L.;* Power Delivery, IEEE Transactions on , Volume: 15 , Issue: 1 , Jan. 2000  
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**19 Comparison of common mode impedance measurements using 2 current probe technique versus V/I technique for CISPR 22 conducted emissions measurements**

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**22 Fault arc propagation on cables for space applications in vacuum, oxygen-enriched air and air**

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*van Helvoort, M.J.A.M.; van Deursen, A.P.J.; van der Laan, P.C.T.;*

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**24 Radiated susceptibility of wiring system aboard lightning struck aircraft. II. Sensitivity to the distributed induced sources**

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*Smith, W.T.; Paul, C.R.; Savage, J.S.; Das, S.K.; Coopriker, A.D.; Frazier, R.K*  
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